



UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE
 United States Patent and Trademark Office
 Address: COMMISSIONER FOR PATENTS
 P.O. Box 1450
 Alexandria, Virginia 22313-1450
 www.uspto.gov

BIBDATASHEET

Bib Data Sheet

CONFIRMATION NO. 4924

SERIAL NUMBER 10/618,376	FILING DATE 07/11/2003 RULE	CLASS 257	GROUP ART UNIT 2811	ATTORNEY DOCKET NO. MIC-M074	
APPLICANTS					
Shekar Mallikarjunaswamy, San Jose, CA;					
** CONTINUING DATA ***** <i>none</i> <i>None</i>					
** FOREIGN APPLICATIONS ***** <i>none</i> <i>None</i>					
IF REQUIRED, FOREIGN FILING LICENSE GRANTED ** 10/08/2003					
Foreign Priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no 35 USC 119 (a-d) conditions <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after Allowance met Verified and Acknowledged <i>Shekar S. S.</i> <i>SL</i> Examiner's Signature Initials		STATE OR COUNTRY CA	SHEETS DRAWING 6	TOTAL CLAIMS 36	INDEPENDENT CLAIMS 3
ADDRESS 32566 PATENT LAW GROUP LLP 2635 NORTH FIRST STREET SUITE 223 SAN JOSE , CA 95134					
TITLE Lateral DMOS transistor with a self-aligned drain region					
FILING FEE RECEIVED 1038	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:			<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit	